FIG. 1

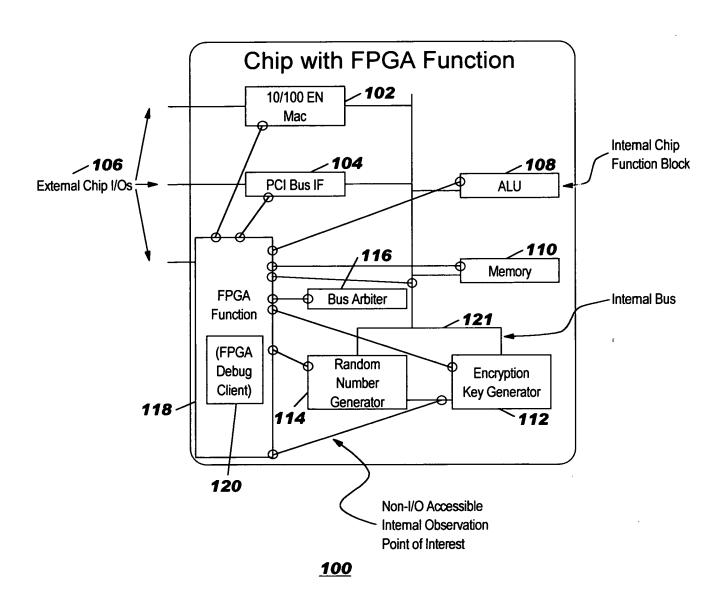
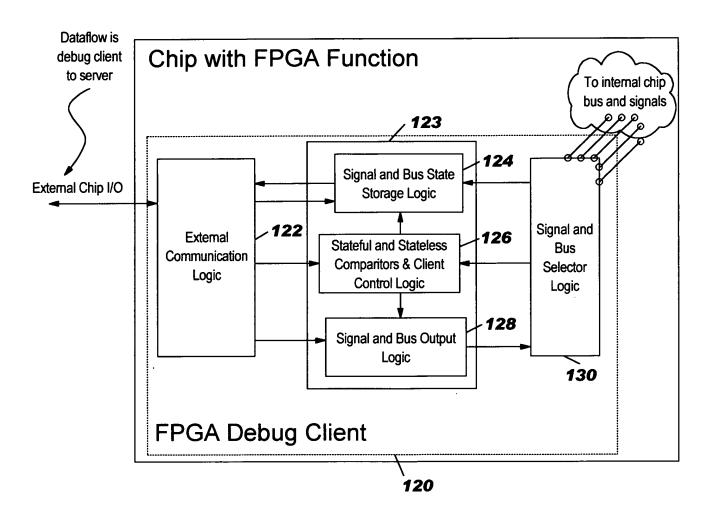


FIG. 2



Internal Chip Function Block - Internal Bus 108 Key Generator Memory Encryption ALC Chip with FPGA Function Non-I/O Accessible Internal Observation Point of Interest 121 102 116 **Bus Arbiter** Random Generator Number 104 10/100 EN Mac PCI Bus IF 100 114 Function (FPGA Debug Client) FPGA 120 Between Debugger Server & Client 240 Physical Debug Connection Programming FPGA Debugger Server 200

FIG. 3

10.34